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Document Listing

Document	Image pages	Text pages	HTML pages	Error Pages
SU 1737455 A 2	0	2	0	0
Total	0	2	0	0

DERWENT-ACC-NO: 1993-150857

DERWENT-WEEK: 199318

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TITLE: Computer program execution monitor has output
of operation code register connected to data input
of first AND=gates unit at first inputs gp of first and
second comparators

INVENTOR: LUCHIN I N; LYASKOVSKII V L ; PROKOFEV YU V

PATENT-ASSIGNEE: CYBERNETICS INST[CYBER]

PRIORITY-DATA: 1990SU-4827374 (May 21, 1990)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE
SU <u>1737455</u> A2	May 30, 1992	RU

APPLICATION-DATA:

PUB-NO	APPL-DESCRIPTOR	APPL-NO
APPL-DATE		
SU 1737455A2	Add to	1990SU-4827374
May 21, 1990		

INT-CL-CURRENT:

TYPE	IPC	DATE
CIPS	G06F11/28	20060101

ABSTRACTED-PUB-NO: SU 1737455 A2

BASIC-ABSTRACT:

The program monitor, as per Parent Cert., additionally includes second OR-gates gp. (13.1-13.N), two comparators (26,27), four AND-gates units (25,28,29), OR-gates unit (30), three AND-gates (16,31,34), three OR-gates (3,6,17), flip-flop (33), address register (14), operation code register (22), counter (18), decoder (19) and second delay element (21). The design allows

efficient
correction of errors produced during failure occurrence while
executing
conditional branching commands.

USE/ADVANTAGE - In computer engineering devices for monitoring
execution of
programs. Improved monitoring fidelity due to operative corrections
of errors.
Bul.20/30.5.92

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CHOSEN-DRAWING: Dwg.1/1

TITLE-TERMS: COMPUTER PROGRAM EXECUTE MONITOR OUTPUT OPERATE CODE
REGISTER

CONNECT DATA INPUT FIRST AND=GATE UNIT GROUP SECOND
COMPARATOR

DERWENT-CLASS: T01

EPI-CODES: T01-J08A; T01-J20;

SECONDARY-ACC-NO:

Non-CPI Secondary Accession Numbers: 1993-115389